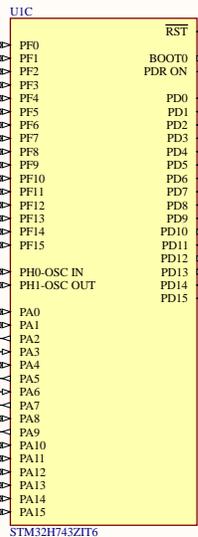
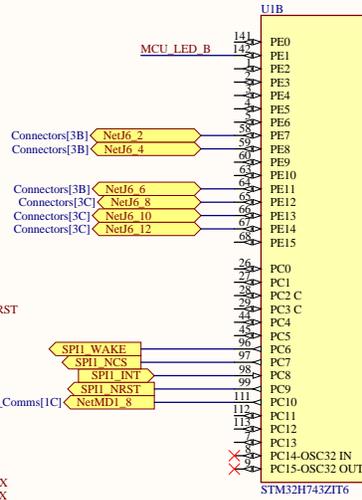
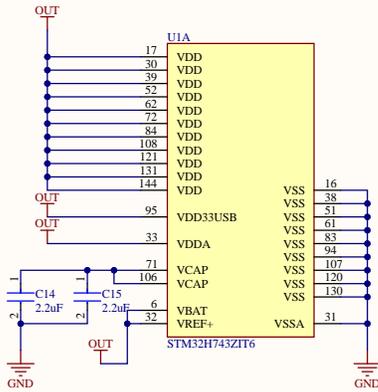
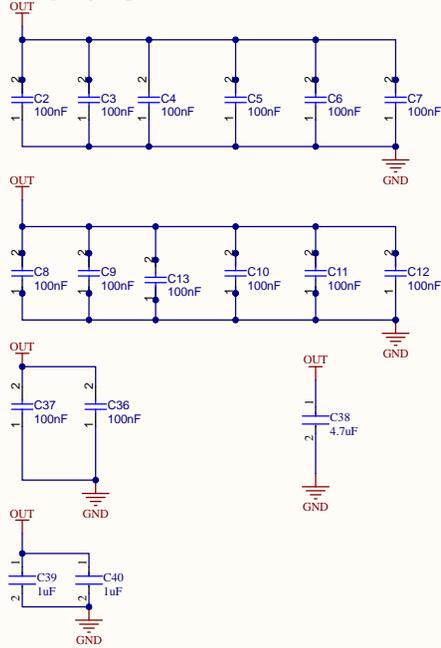
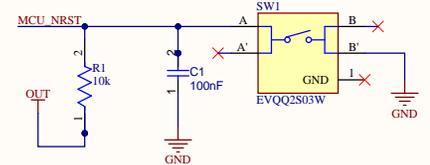


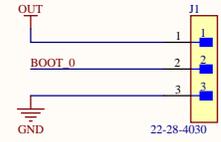
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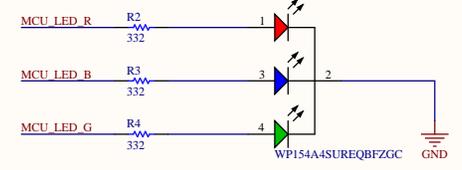
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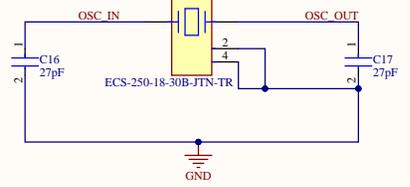
BOOT0 Switch



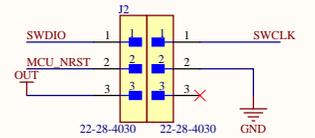
LEDs



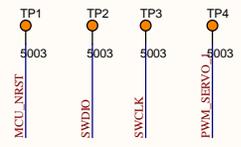
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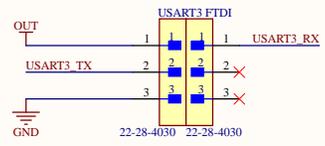
SWD Connector/JTAG



MCU Debug TPs

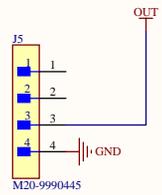


J_DEBUG

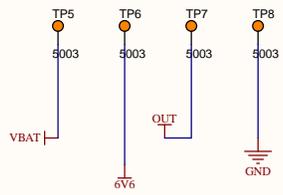


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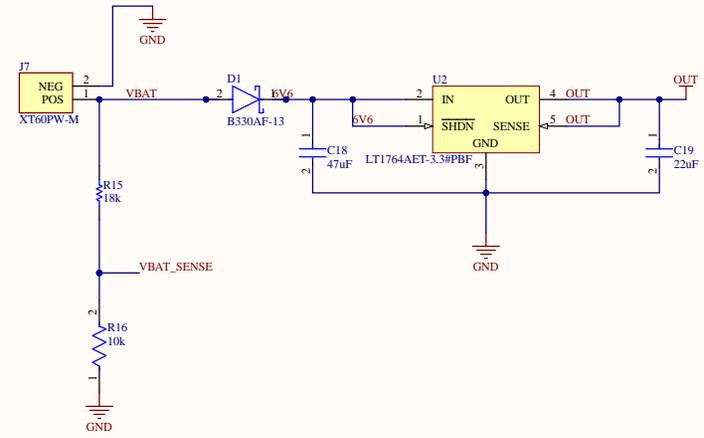
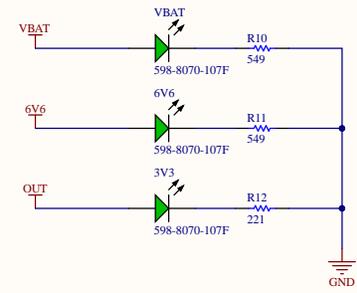
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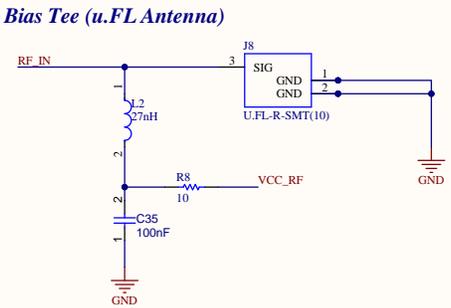
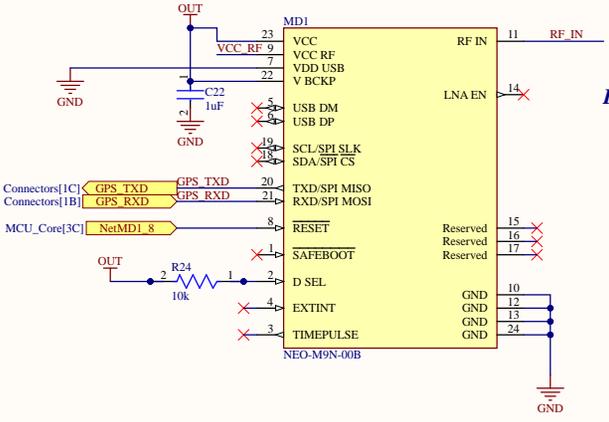
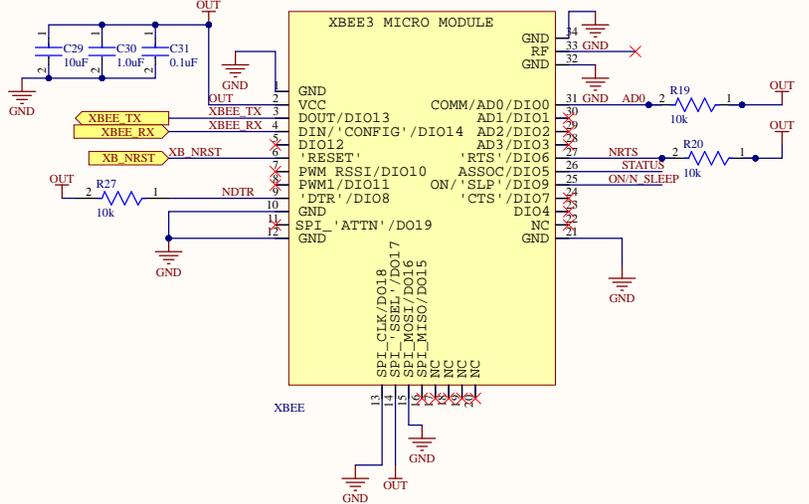
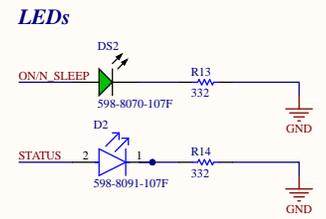
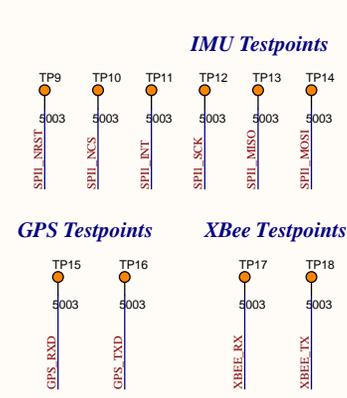
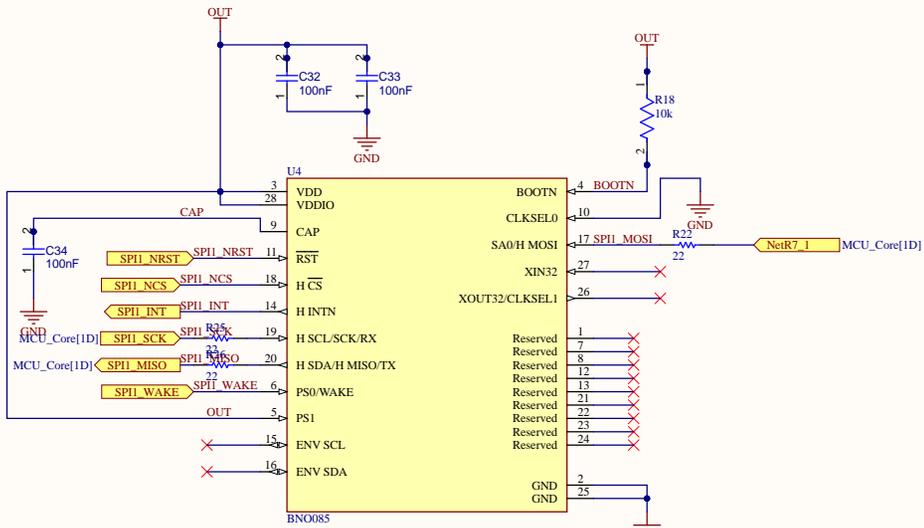
Power Rail TPs



Power Rail LEDs

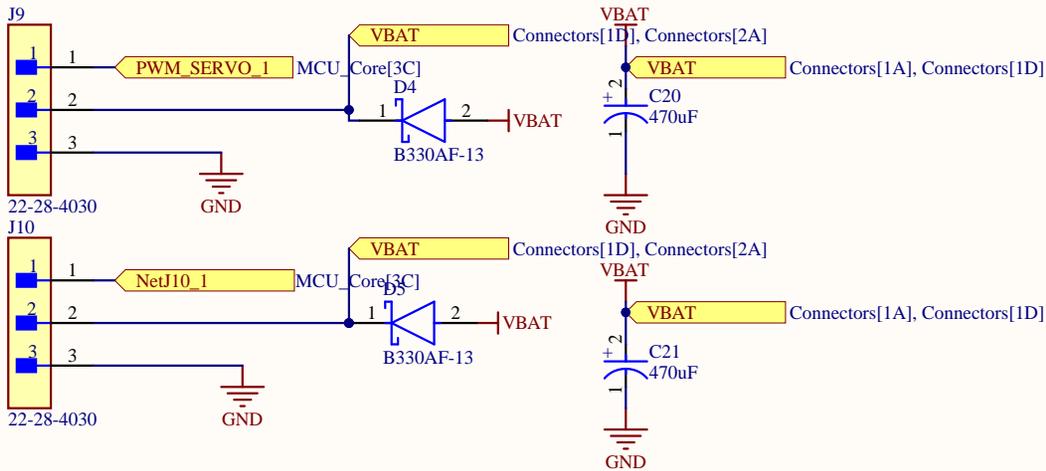


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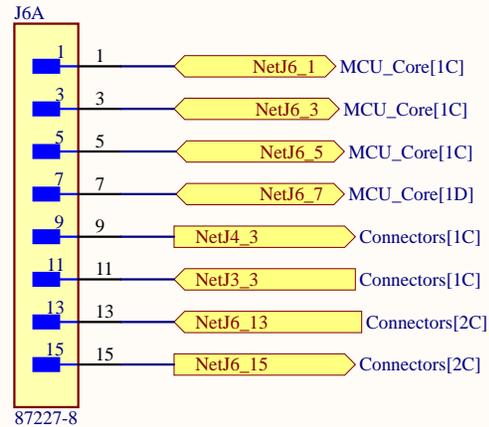


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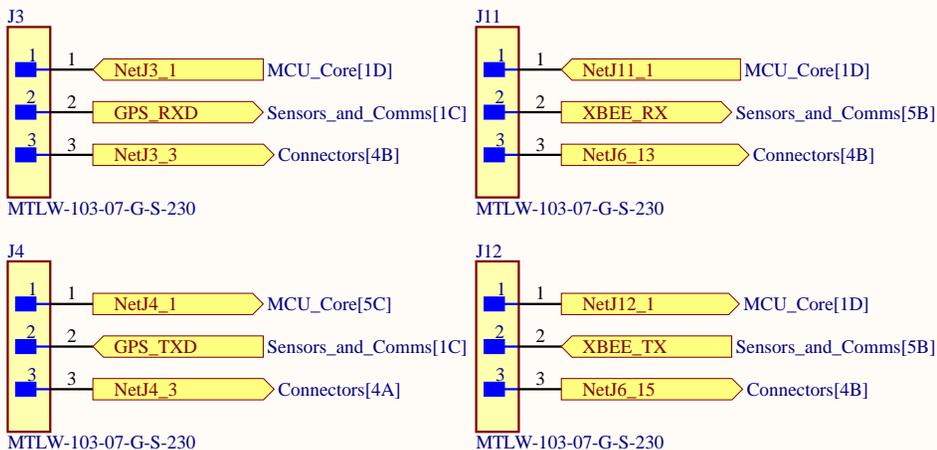
Extra GPIO



A

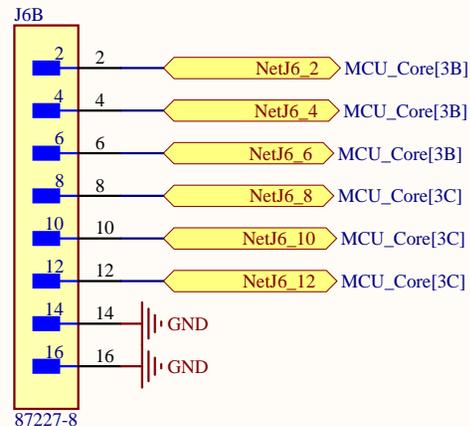
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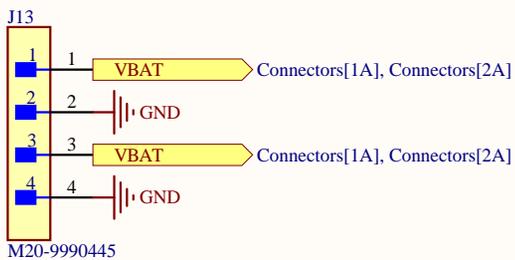
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C



C

D



D

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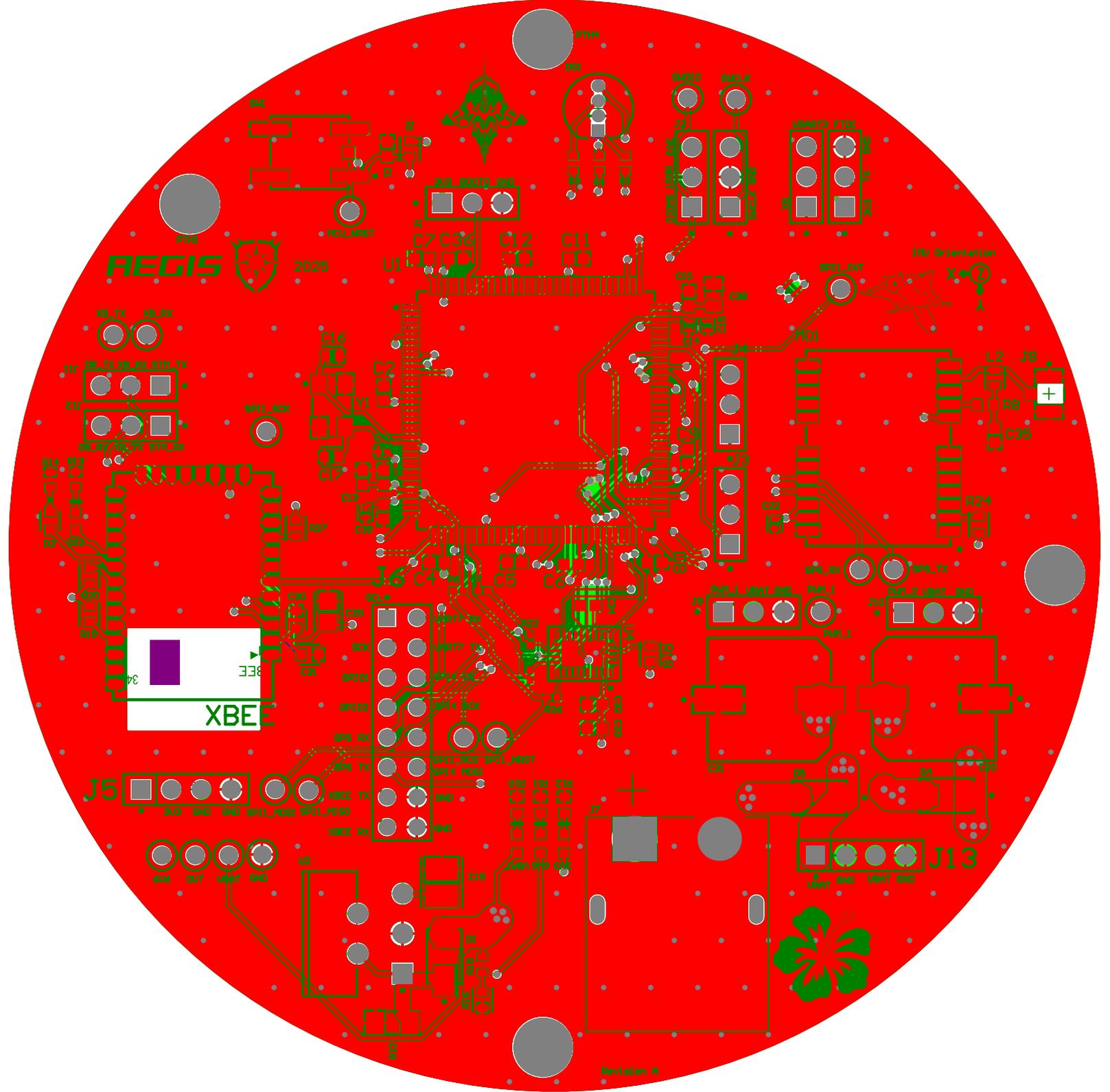
REGIS



2025



Revision A



Board Stack Report